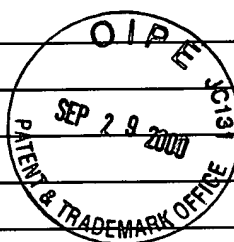


FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE STATEMENT          BY APPLICANT</b>  (USE SEVERAL SHEETS IF NECESSARY)	ATTY. DOCKET NO. ARC.001A	APPLICATION NO. 09/418,663
	APPLICANT Hakewill, et al	
	FILING DATE October 14, 1999	GROUP 2763

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
EG	6	5,491,640	02/13/96	Sharma, et al.	-	-	
+	7	5,493,508	02/20/96	Dangelo, et al.	-	-	
	8	5,502,661	03/26/96	Wolfgang Glunz	-	-	
	9	5,537,580	07/16/96	Giorni, et al.	-	-	
	10	5,544,067	08/6/96	Rostoker, et al.	-	-	
	11	5,555,201	09/10/96	Dangelo, et al.	-	-	
	12	5,801,958	09/01/98	Dangelo, et al.	-	-	
	13	5,841,663	11/24/98	Sharma, et al.	-	-	
	14	5,854,930	12/29/98	McLain, et al.	-	-	
	15	5,898,595	04/27/99	Bair, et al.	-	-	
	16	6,026,219	02/15/00	Miller, et al.	-	-	
EG	17	6,110,223	08/29/00	Southgate, et al.	-	-	



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## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

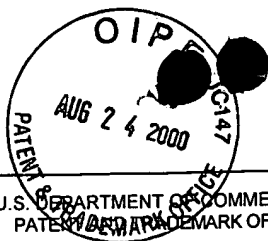
## OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

EXAMINER INITIAL	

EXAMINER ED GARCIA-OTERO

DATE CONSIDERED 7/25/02

\*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT Hakewill, et al	
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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
EG	1	5,812,416	09/22/98	Gupte, et al.	/	-	

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
EG	2	WO 97 13209 A	April 1997	WIPO	-	-		

## OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

EXAMINER INITIAL		OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
EG	3	Elms, A., "Tuning a Customisable RISC Core for DSP," Electronic Product Design, Sept. 1997, Vol. 18, No. 9, pages 19-20, 22, XP000909039
EG	4	Berekovic, Mladen, et al., "A Core Generator for Fully Synthesizable and Highly Parameterizable RISC-Cores for System-On-Chip Designs, 1998 IEEE Workshop on Signal Processing Systems, Pages 561-568, XP-002137267
EG	5	Yang, Jin-Hyuk, et al., "MetaCore: A Configurable & Instruction-Level Extensible DSP Core," Proceedings of the ASP-DAC '98 Asian and South Pacific Design Automation Conference 1998, pages 325-326, XP-002137268

EXAMINER	EDUARDO GARCIA-OTERO	DATE CONSIDERED	7/25/02
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\* DOCUMENT #3 IS NOT LEGIBLE. EG. 7/25/02